

Electronic Patent Application Fee Transmittal

Application Number:	10702437			
Filing Date:	07-Nov-2003			
Title of Invention:	Substrate-biased silicon diode for electrostatic discharge protection and fabrication method			
First Named Inventor:	Chyh-Yih Chang			
Filer:	Clark A. Jablon/Gladys E. Morales			
Attorney Docket Number:	06720.0064-02			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Submission- Information Disclosure Stmt	1806	1	180	180
Total in USD (\$)				180